

Fall 2008

VIRGINIA TECH

ECE 2274 ELECTRONIC NETWORKS LAB I

LAB SYLLABUS

Lab Supervisor: Dennis Newman

E-mail: dnewman@vt.edu

Mailbox: 236 Whittemore Hall

Office Hours: TBA

Lab Instructor:

I. Course Material:

Laboratory Projects will be available at

<http://www.ece.vt.edu/ece2274/>

II. Lab Objectives:

- To gain practical understanding of circuit theory and electronic components.
- To become familiar with basic laboratory equipment.
- To become proficient using P-spice.

III. Grading:

Grade will be divided as follows:

| | |
|-----------------|-----|
| Pre-lab reports | 50% |
| Lab Report | 30% |
| Final Exam | 20% |

IV. Notebooks:

Students are encouraged to keep a lab notebook for notes.

V. Pre-Labs:

Each student is required to complete and turn in the pre-lab at the beginning of the lab. Failure to turn in the pre-lab will result in an “O” grade for that particular pre-lab. No late pre-labs will be accepted unless prior approval. Some pre-labs are lengthy; so do not wait until the last minute to start the pre-labs.

VI. Lab Reports:

Each lab report must be completed at the scheduled lab period. Missing a scheduled lab without prior approval will result in a 10% penalty per day for that particular lab.

VII. P-Spice:

Since P-spice is required to complete many of the pre-labs, **YOU MUST BE PROFICIENT IN P-SPICE.**

VIII. Honor Code:

The Honor Code will be enforced in this class. Discussion of topics with other people in the class is fine, but all submitted work should be your own.